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*Admitted only in Maryland
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Federal Agencies

August 2, 2005

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Commissioner for Patents
PO Box 1450
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Re: U.S. Utility Patent Application
Appl. No.: 09/610,798; Filed: July 6, 2000
For: **Distributed Processing in a Cryptography Acceleration Chip**
Inventors: Suresh KRISHNA *et al.*
Our Ref: 1875.4310003

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Reply Under 37 C.F.R. § 1.116; and
2. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

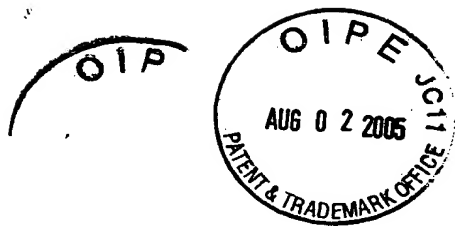
Respectfully submitted,

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RES/LAG:ddc
Enclosures

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Reply Under 37 C.F.R. § 1.116
Expedited Procedure – Art Unit 2164

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Suresh KRISHNA *et al.*

Appl. No.: 09/610,798

Filed: July 6, 2000

For: **Distributed Processing in a
Cryptography Acceleration Chip**

Confirmation No.: 4877

Art Unit: 2164

Examiner: Ortiz, Belix M.

Atty. Docket No.: 1875.4310003

Reply Under 37 C.F.R. § 1.116

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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Sir:

In reply to the Office Action dated June 2, 2005, Applicants submit the following
Remarks.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.